

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

10/708,067

Title of Invention

Method of Manufacturing a Semiconductor Device and a Method
for Fixing the Semiconductor Device Using Substrate Jig

Application Number :

Confirmation Number:

First Named Applicant: Kazuo TESHIROGI

Attorney Docket Number: 020334A

Art Unit:

Examiner:

Search string: (6037026 or 5631191 or 4016829 or 5458755).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
TL	1	6037026	2000-03-14	Iwamoto			
TL	2	5631191	1997-05-20	Durand et al.			
TL	3	4016829	1977-04-12	Ito et al.			
TL	4	5458755	1995-10-17	Fujiyama et al.			

Signature

Examiner Name	Date
	8/18/04



INFORMATION DISCLOSURE CITATION PTO-1449	Atty. Docket No. 020334A	Serial No. 10/708,067
	Applicant(s): TESHIROGI, Kazuo et al.	
	Filing Date: February 6, 2004	Group Art Unit:

U.S. PATENT DOCUMENTS

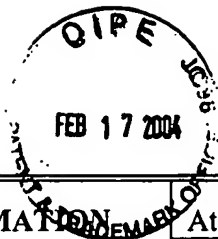
Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
____	AA					
____	AB					
____	AC					
____	AD					
____	AE					

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
<u>R</u> AF 11-307488	11/05/99	Japan	Abstract only
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____ AH			
____ AI			
____ AJ			

OTHER DOCUMENTS

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Examiner	<u>Thack</u>	Date Considered	<u>8/18/04</u>



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OTHER DOCUMENTS

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_____	AL	
Examiner <u>Thane</u>		Date Considered <u>8/18/04</u>